



**SCHEMATIC DIAGRAM
MODEL 79-007**

NOTE:
 Δ VOLTS AT TRANSMIT CONDITION
 ALL VOLTAGES MEASURED FROM PC BOARD GND WITH DC VTVM AT NO SIGNAL (AT 13.8V POWER SUPPLY) IF MEASUREMENT VALUES OBTAINED ARE IN EXCESS OF ±20% OF VALUES SHOWN THEN REASON FOR DIFFERENCE SHOULD BE CORRECTED
 ⊥ CHASSIS GND * PC BOARD GND
 * VARIABLE

TRANSISTOR BASE INFORMATION E EMITTER C COLLECTOR B BASE S SOURCE G GATE D DRAIN

